### The Future of Advanced Computing: It's Uncertain



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### Discussion roadmap

Changing nature of science

**Technology lessons and exponentials** 

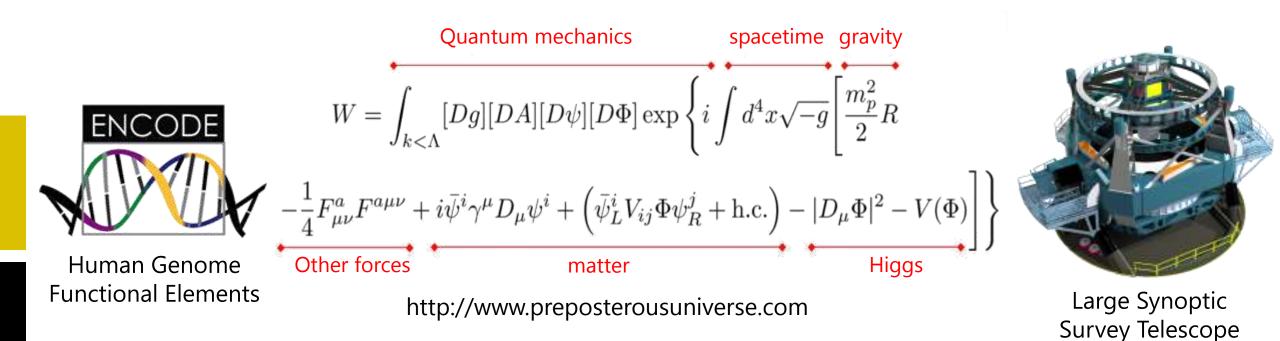
**Re-conceptualizing advanced computing** 

Thoughts on the future

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Brain Connectome



Ocean Observing System



Square Kilometer Array THE UNIVERSITY OF IOWA

### Square Kilometer Array (SKA) BIG, Big, big data

#### Structure

- SKA-MID (South Africa) with ~2000 15m dishes
- SKA LOW (Australia) with ~1 million dipole antennas
- Spread over 3000 km with construction in 2018 ...
- 50 MHz 14 GHz coverage; 100X sensitivity; 10<sup>6</sup> faster

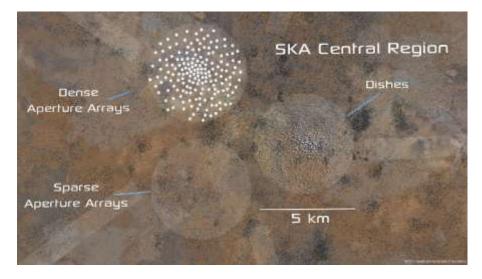
### Science

- Galaxy evolution and dark energy
- Gravitational waves
- Dark Ages look back

#### Data and processing

- From 100 PB/day to 10,000 PB/day
- Real-time reduction at exascale levels
  - World's biggest FFTs









### Ocean Observing Initiative (OOI) Heterogeneous data



#### Structure

- Coastal and Global Scale Nodes (CGSNs)
  - Buoys, moorings, cables and AUVs
- Regional Scale Nodes (RSNs)
  - 900 km of cabled sensor arrays

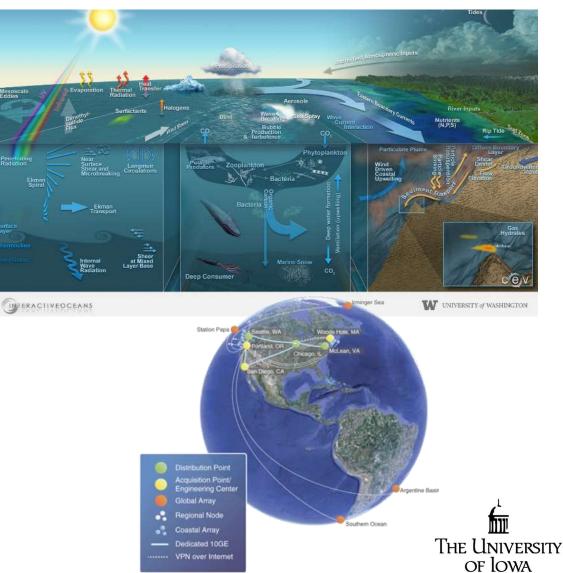
#### Science

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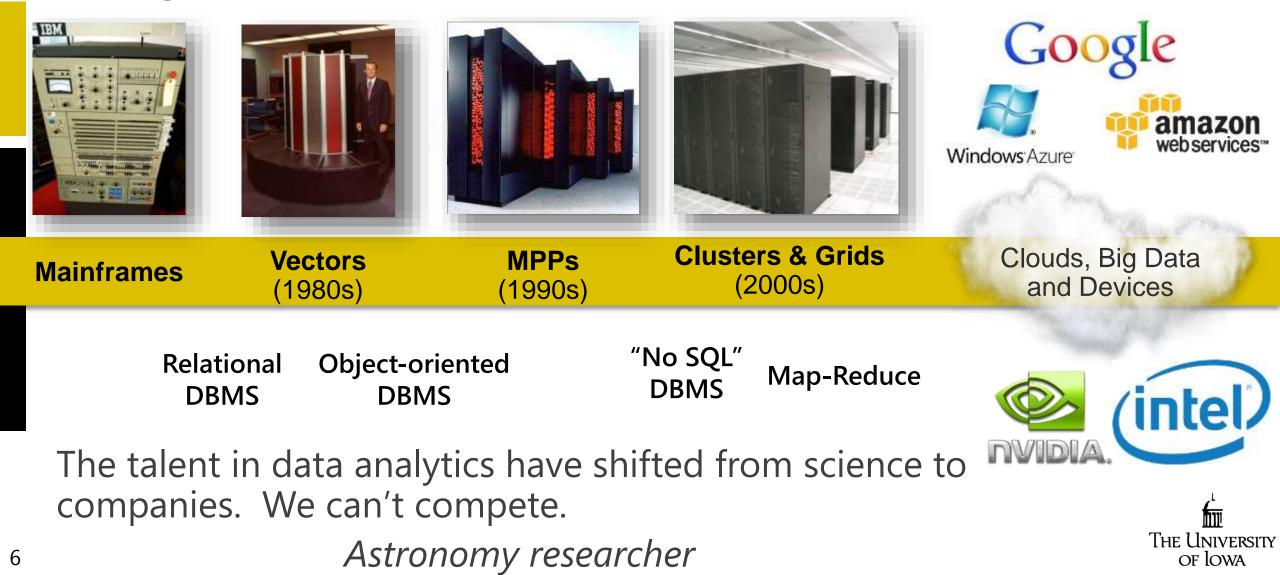
- Ocean-atmosphere exchange
- Climate variability
- Turbulent mixing and biophysical interactions
- Coastal ocean dynamics
- Geophysics and geodynamics

#### Data and processing

- Extremely heterogeneous data
- Complex software and data products



### Computing transitions: Riding the economic wave



### Quantitative begets qualitative

#### Four computing capabilities

- Networking delivering questions and answers
- Computation transforming data into insights
- Data access timely access to data by computations
- Storage long-term data retention

#### Three defining attributes ...

• Speed, capacity and cost ... and their ratios determine viability

#### Exponentials are very deceptive

- Early, the future looks like the past
- Suddenly, everything is different





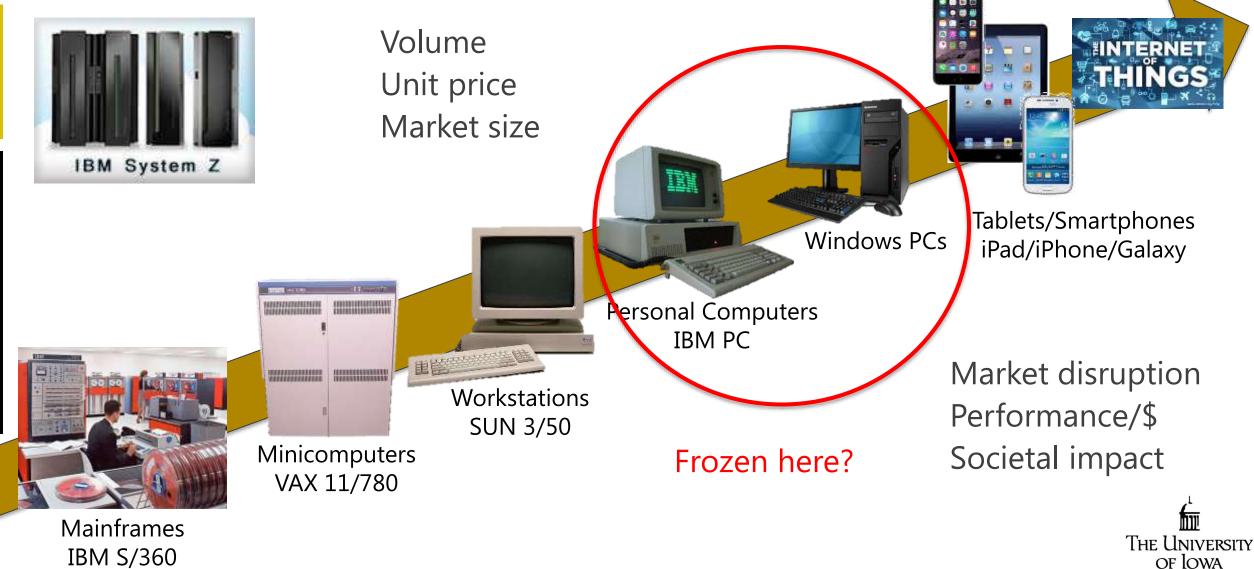
Seagate 2 TB Disk



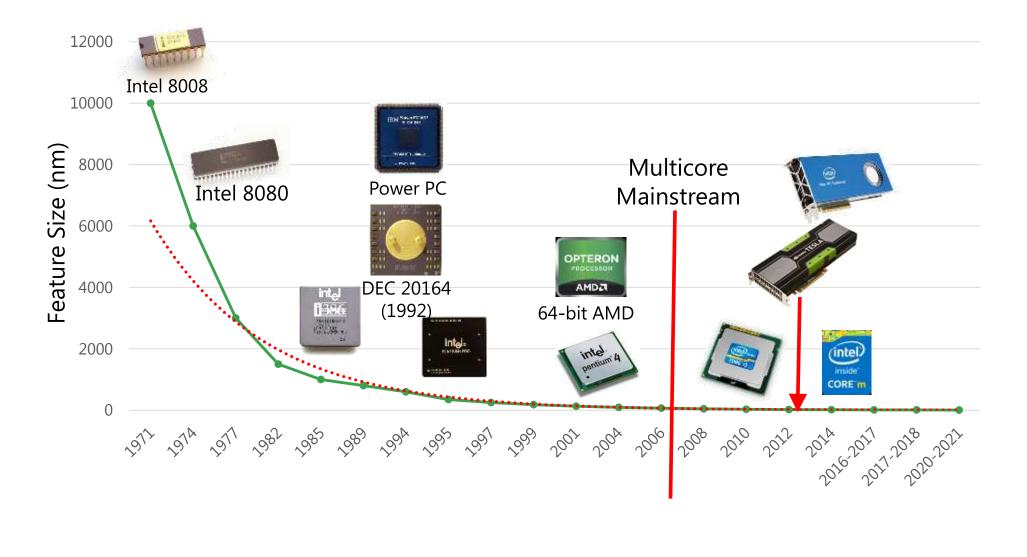


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### Generation after generation Disrupted from below by 10X



### Chip feature sizes No exponential is forever (except in the textbooks)



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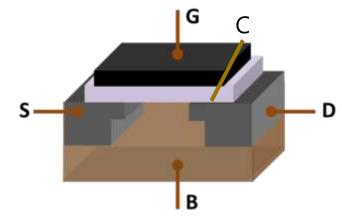
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### Room at the bottom: less and less Yeah, we did that – Dennard scaling has come and gone

Why can't we make them [computers] very small, make them of little wires, little elements – and by little, I mean *little*. For instance, the wires should be 10 or 100 atoms in diameter, and the circuits should be a few thousand angstroms across. Everybody who has analyzed the logical theory of computers has come to the conclusion that the possibilities of computers are very interesting – if they could be made to be more complicated by several orders of magnitude.

> Richard Feynman December 29, 1959



MOSFET (Wikipedia) G (gate), S (source) D (drain) and C (channel)

 $CV^2F+S$ 



#### Cramming more components onto integrated circuits

With unit cost falling as the number of components per circuit rises, by 1975 economics may dictate squeezing as many as 65,000 components on a single silicon chip

By Gordon E. Moore Director, Research and Development Laboratories, Faincheld Semiconductor tation of Patrohike Cameria and Institument C

The future of integrated electronics is the fature of electronmachine inneal of being concentrated in a central arit. In ics mult. The advantages of incentation will bring about a addition, the improved reliability reads possible by improved proliferation of elimitrarias, pushing this science into many citaats will allow the communities of hegor processing units. -

computers - or at lass terminals constanted to accented contiputer-automatic controls for automobiles, and perional portable communications equipment. The electronic wristwatch needs only a display to be feasible asday.

But the biggest potential lies in the production of large systems. In adaptions communications, integrated circuitsin digital filters will separate channels on realization aquipment. Relighted circuits will also switch telephone circuits. and perform data processing.

Computers will be more presenful, and will be organized in completely different ways. For example, memories built of integrated elactronics may be distributed throughout the



in Gampion E. Mason in one of te new broad of electronic regressive, schooled in the sizal sciences rather the ectronics. He earlied a B.B. spree in charmentry from the overally of California and a 1.D. degree in physical sensitive from the California without of Tachnology. He was no of the foundation of Patrichi fuctor and has been ractor of the research and

Machines similar withose in avoidance today will be built at Integrated circuits will lead to such wonders as home - lower costs and with faster tars-around. By integrated electronics, I mean all the various technologies which are referred to as microelectronics today as

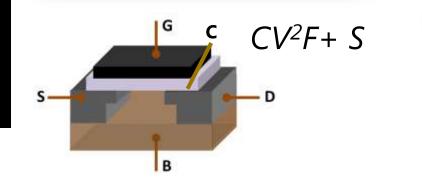
well as not additional once that result in electrowise firsttions supplied to the user as treducible units. These techstelegies were first investigated in the late 1950's. The objest was to miniaturize electronics equipment to include inreasingly complex electronic functions in limited space with ninimum weight. Several approaches evolved, including microgenerably techniques for enforched concernants that film serveneres and semiconductor integrated circuits. Each approach evolved topidly and converged so that

each have wed techniques from another. Many researchers believe the way of the future to be a combination of the varirun approaches. The advocutor of persistentiation integrand situation and

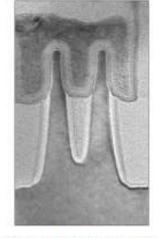
already using the improved characteristics of this-film revistors by applying such films directly to an active semiconduc tor substrate. These advocating a technology based upon Hims are developing sophisticated techniques for the stack-ment of active sumkconductor devices to the possive Elman-

Bolk approaches have worked well and are being used in aquipment hider.

Electronics, Volume 38, Number 8, April 19, 1965





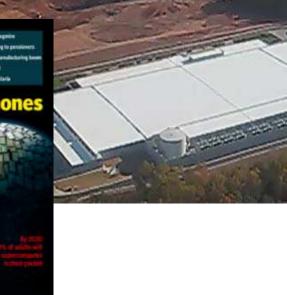


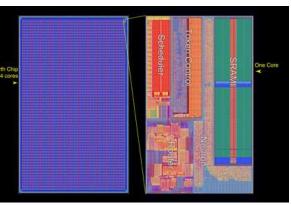
14 nm 2<sup>nd</sup> Generation Tri-gate Transistor



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#### **Planet of the phones**





IBM True North chip 4096 cores x 256 neurons

Inflection points nearby

### A bit of inflection point history NCSA terascale and PlayStation2 clusters (~2001)

1 TF IA-32 Pentium III cluster (Platinum)

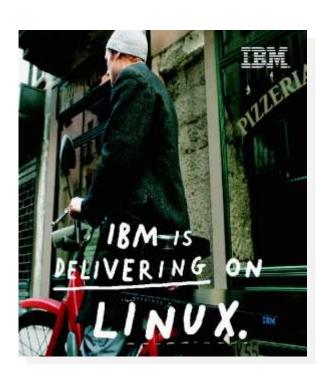
- 512 1 GHz dual processor nodes
- Myrinet 2000 interconnect and 5 TB of RAID storage
- 30th on Top500 list (world's fastest Linux cluster)

#### 1 TF IA-64 Itanium cluster (Titan)

- 164 800 MHz dual processor nodes
- Myrinet 2000 interconnect





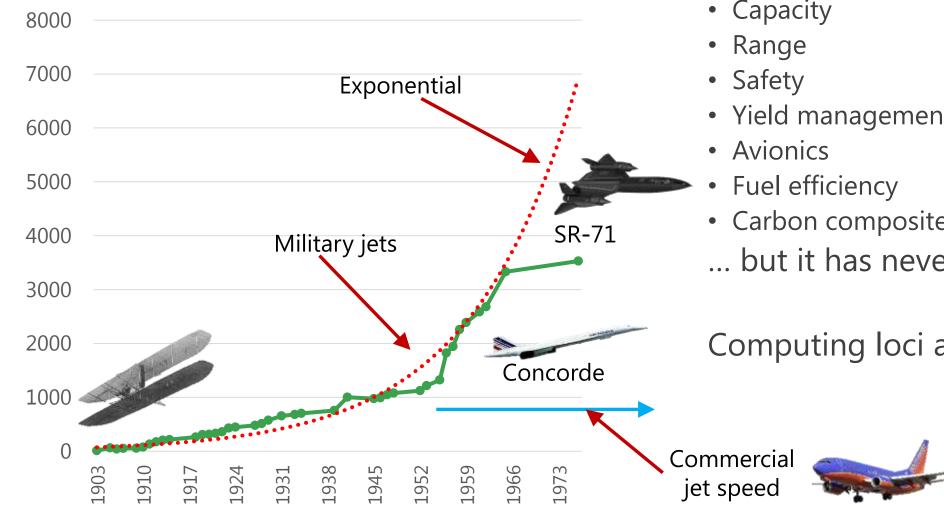








### A lesson from another domain Airspeed records (kilometers/hour)



#### Loci of airplane innovation shifted

- Capacity
- Yield management
- Carbon composites
- ... but it has never stopped

Computing loci are also shifting

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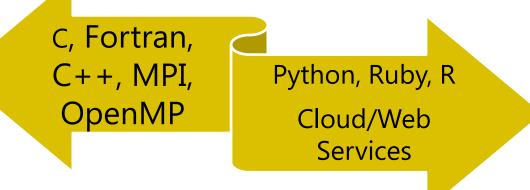
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# Diverging cultures

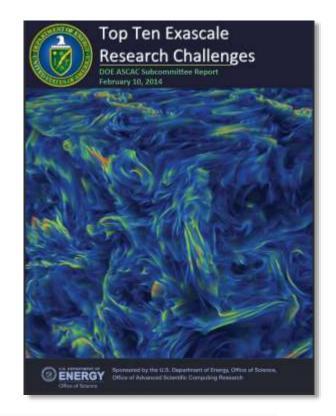
Technical application complexity is rising

- Multidisciplinary fusion
- Temporal and spatial adaptation
- Data assimilation and processing
- ... along with multiple optimization axes
- Massive parallelism with heterogeneous cores
- Resilience/reliability at large scale
- Energy optimization for utility

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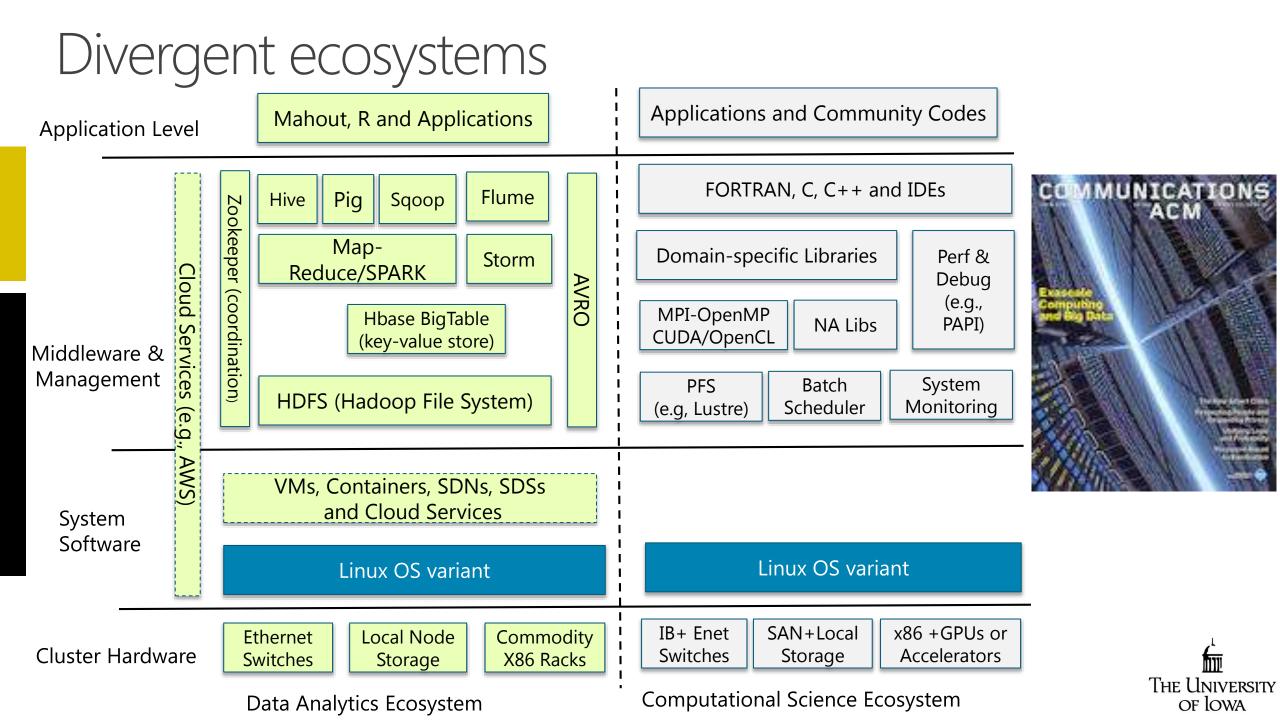


Technical and mainstream software development have diverged





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## Recommender techniques

#### Item hierarchy (Amazon)

- You bought a Kindle<sup>™</sup>, you'll want a cover Attributes (Pandora)
- You like 70's pop, you'll like *Simon and Garfunkel* Item similarity (Netflix)
- You liked *Batman*, you'll like *Spiderman* User similarity (Walmart)
- People who buy beer generally buy chips
  Social network (Linkedin)
- Your connections liked this job posting, so will you
  Model based (HPC challenges and needs)
- Training, singular value decomposition (SVD), support vector machines (SVM)
- Supervised and unsupervised learning ...

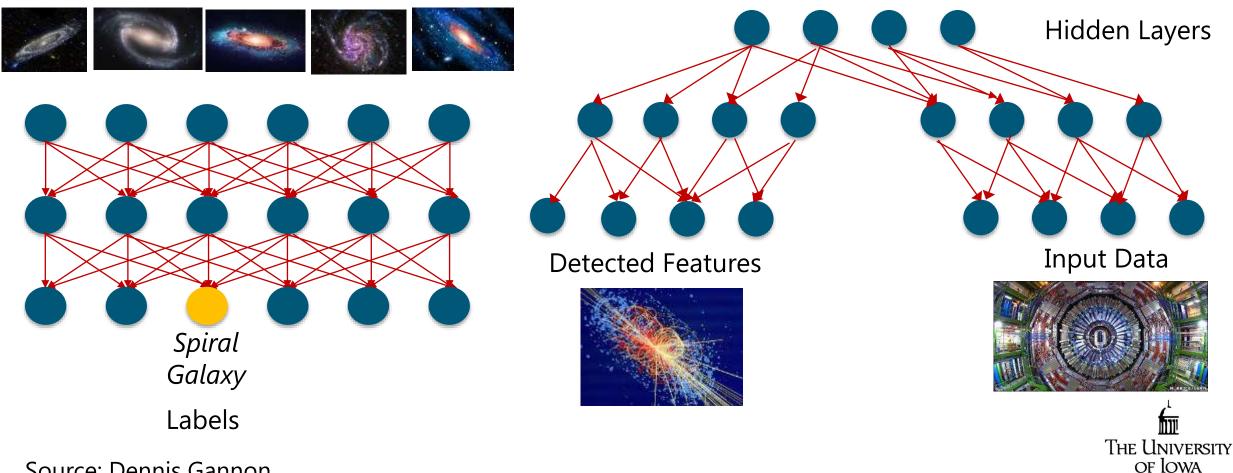




### A machine learning revolution is underway – if we let it

Big data and massive parallelism

- Supervised machine learning inference from labeled training sets
- Unsupervised machine learning finding structure in data without labels



Source: Dennis Gannon





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Requested compute and storage for *ten years*?

Logged onto a node and killed processes just to see what would happen?

Wished you could load containers rather than just applications?

Found your code performance limited by the I/O bandwidth of a Raspberry Pi?

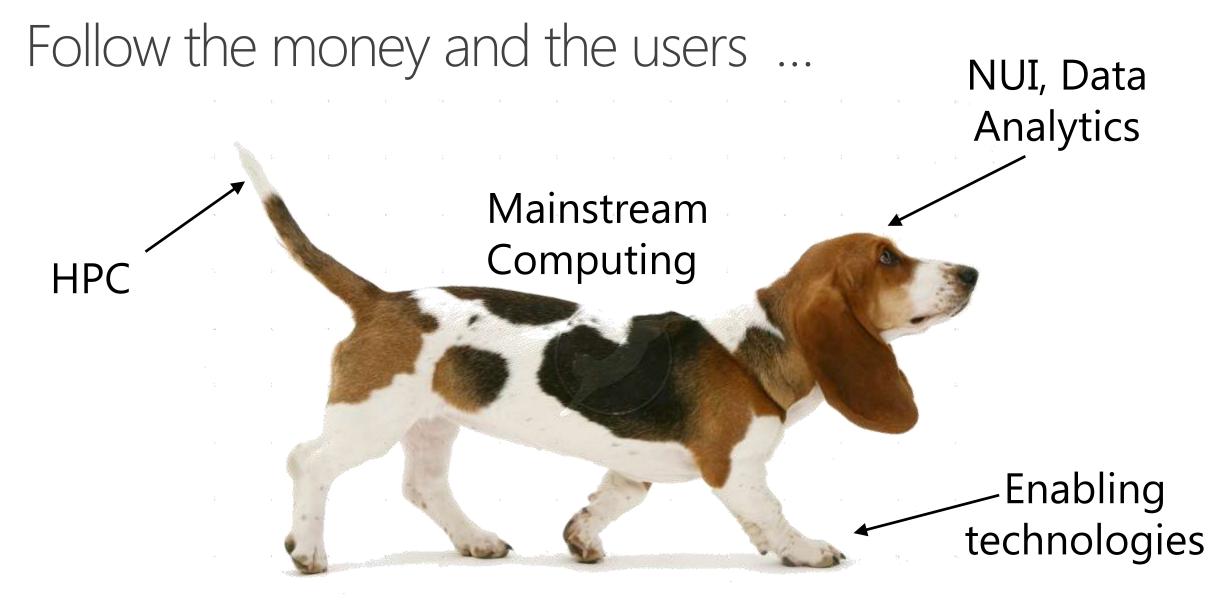
Thought SAN was just a typo in a message meant for Sam?

Asked your system for recommendations?

Wondered why R came after S and C doesn't matter?



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 $\dots$  or the money and the users may not follow you  $\mathbf{f}_{\text{THE UNIVERSITY}}$ 



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### Clouds 101 It's a bird, it's a plane ...

Some of us are old enough to remember public timesharing ...

Data centers built for Internet services

• Search, email, e-commerce ...

Infrastructure optimization at scale

- Cloud software stacks, data centers, CDNs
- Virtualization, containers, SDNs, IaaS/PaaS/SaaS

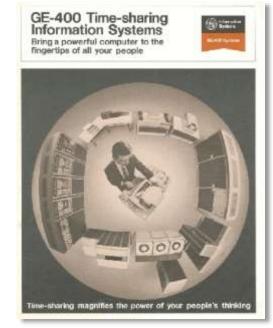
Rise of deep learning

• Big data and analytics





Google



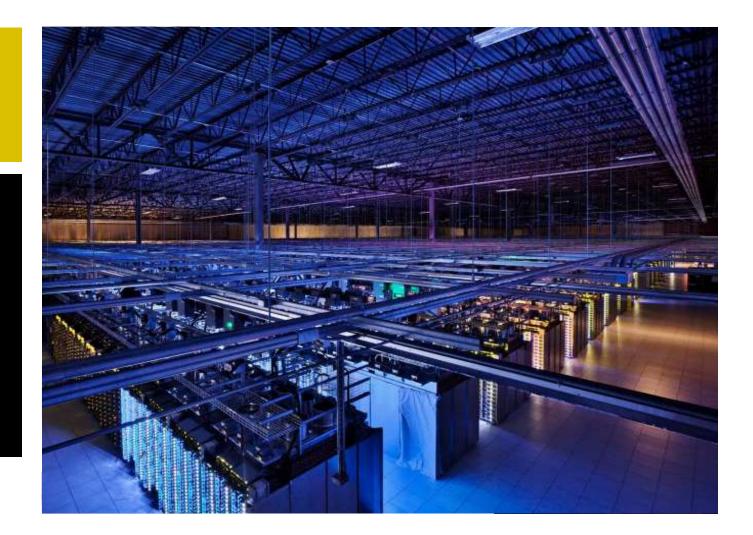


**Microsoft Azure** 

amazon

webservices™

### Cloud data centers: The iceberg view



It's all about ratios ...

• Cost, performance, capacity

#### Jim Gray's four axes

- Networking
- Computation
- Storage
- Access
- ... and that is still driving change
- Modular building blocks
- Component integration
- Systemic resilience
- Cooling and power
- SDN and SDS, containers















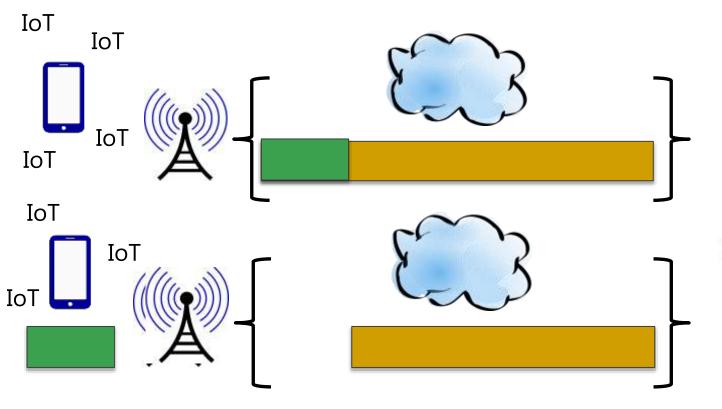




### Natural Interfaces and Intelligent Assistants



### Optimizing the continuum

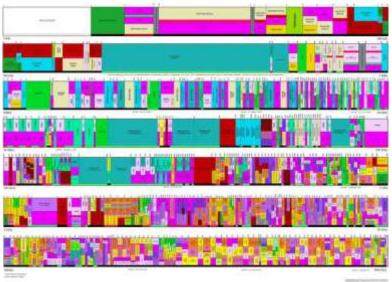


Latency Storage Bandwidth Knowledge Energy Context

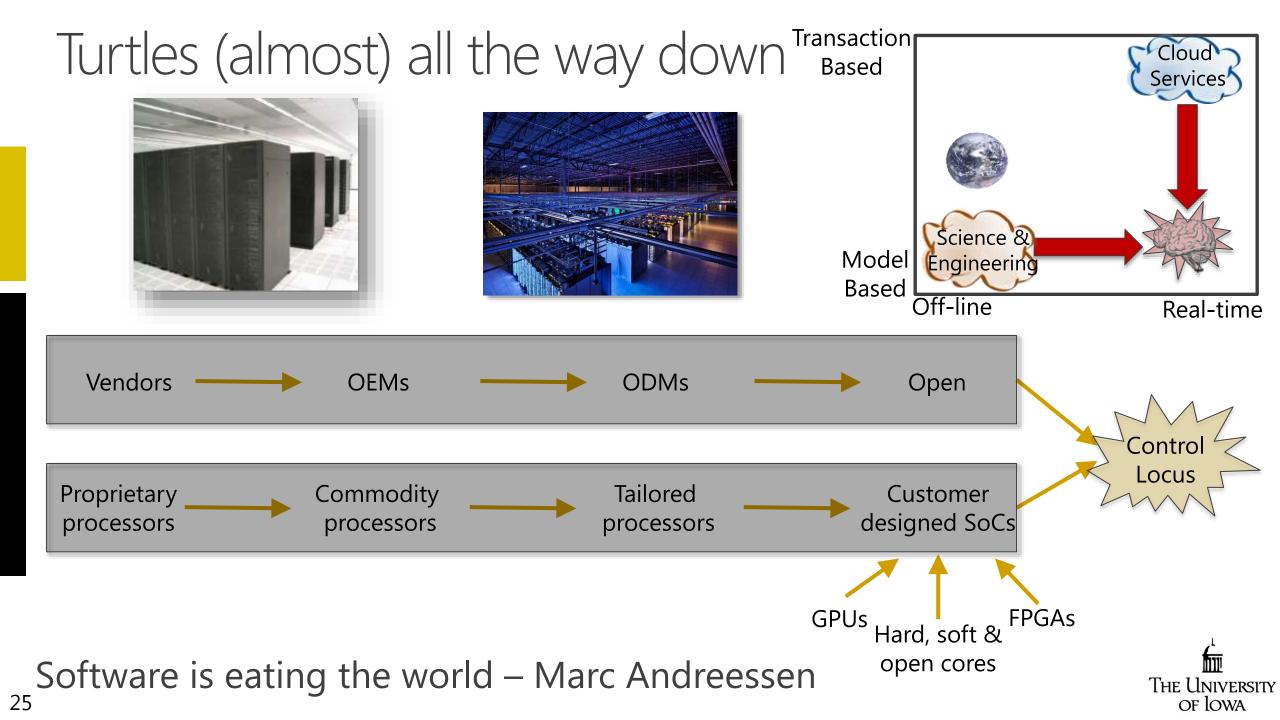


#### Think about augmented reality









### Software is eating the world

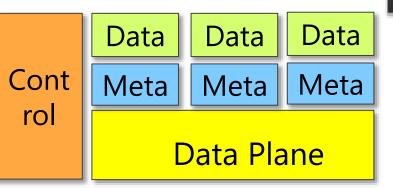
Containerization (OS virtualization)

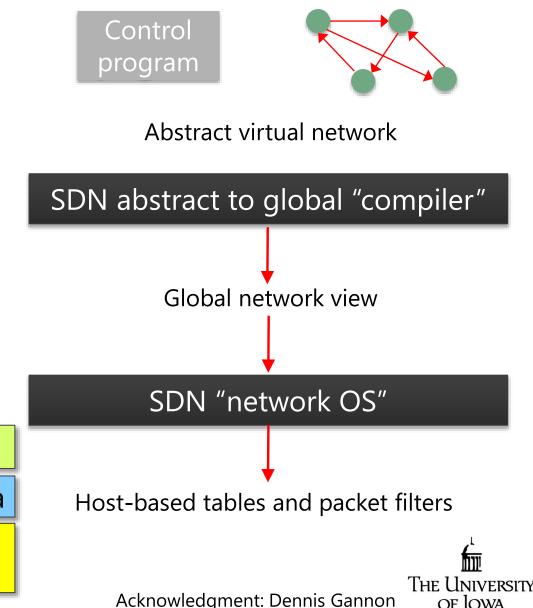
Software defined networks (SDNs)

- Merchant silicon revolution
- Network control
  - Uniform abstractions and separation of concerns
  - Control functions in OS not network switches

#### Software defined storage

- Abstraction and separation
- Storage virtualization
- Policy management

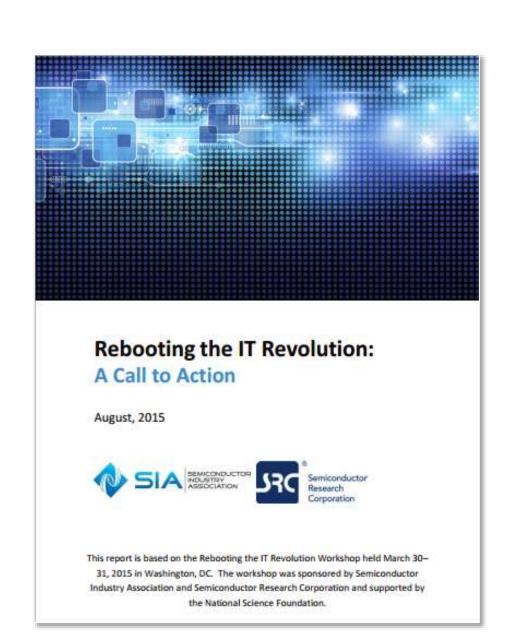




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### Rebooting the IT Revolution

Energy efficient sensing and computing Cyber-physical systems Intelligent storage Real-time communication ecosystem Multilevel and scalable security Next-generation manufacturing paradigm Insight computing





# Computing energy efficiency

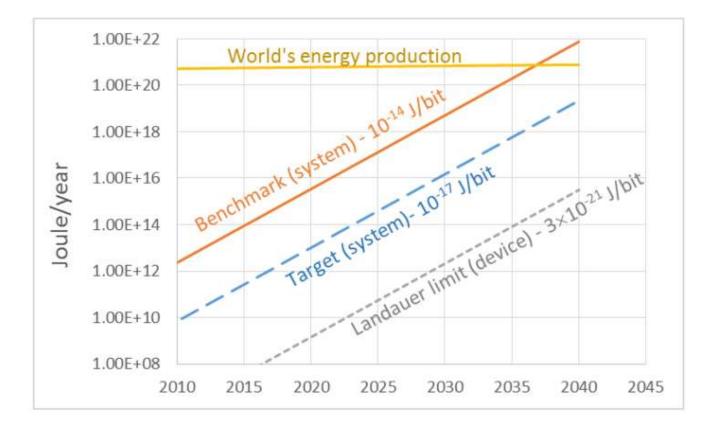
#### 100 picojoules can

- Run a Cortex M0 for 10 cycles
- Write 1 bit of FLASH
- Write 300 bits of SRAM or DRAM
- Send 5 bits across LPDDR4
- Transmit 2 bits of UWB data
- Transmit 0.02 bits over Bluetooth LE

#### Remember

• 1 KW is 3.6 megajoules

#### We have to be more efficient





## Analog brains and digital computers

Parallel distributed architecture Low power (25W) Small volume (1 liter) Asynchronous Analog computing Integrated memory/computation Intelligence via learning Noisy components Low speed operation Spontaneously active



Mostly serial architecture High power (100 MW) Large footprint (40M liters) Synchronous **Digital computing** Separated memory/computation Intelligence via programmed rules **Precise components** High speed operation Passive unless instructed



### Shifting computing perspectives An error prone experiment

Accept inaccuracy/errors as the norm

• Verification and validation (V&V)

Computation as an {un}biased sample

• "An" approximate answer rather than "the" answer

#### Many axes

- Variable precision arithmetic
- Bit error tolerance
- Software correctness
- Energy adaptivity



#### Analog, noisy inputs

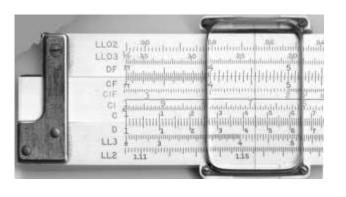
Sensors

#### Analog, error tolerant outputs

• Multimedia, data classification

#### Multiple acceptable answers

• Web search, climate models



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# Discover

Dare to Discover

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### Thank You

