

Richard Vuduc

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Bio:

Richard (Rich) Vuduc is an Associate Professor at the Georgia Institute of Technology ("Georgia Tech"), in the School of Computational Science and Engineering. His research lab, the HPC Garage (hpcgarage.org), is interested in all-things-high-performance-computing, with an emphasis on parallel algorithms, performance analysis, and performance tuning. He is a member of the DARPA Computer Science Study Panel, recipient of the NSF CAREER Award, and co-recipient of the Gordon Bell Prize (2010). For his teaching activities, he received a Lockheed Martin Excellence in Teaching Award (2013). His lab's research has received a number of best paper nominations and awards, including most recently the 2012 Best Paper Award from the SIAM Conference on Data Mining.